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QUERY CONTROL FORM		RTIS USE ONLY	
Application No. <u>09/978181</u>	Prepared by <u>SM</u>	Tracking Number <u>05883763</u>	
Examiner-GAU <u>Weier - 1761</u>	Date <u>1/23/04</u>	Week Date <u>1/5/04</u>	
	No. of queries	IFW	

## JACKET

a. Serial No.	f. Foreign Priority	k. Print Claim(s)	p. PTO-1449
b. Applicant(s)	g. Disclaimer	l. Print Fig.	q. PTOL-85b
c. Continuing Data	h. Microfiche Appendix	m. Searched Column	r. Abstract
d. PCT	i. Title	<u>n. PTO-270328</u>	s. Sheets/Figs
e. Domestic Priority	j. Claims Allowed	o. PTO-892	t. Other

## SPECIFICATION

- a. Page Missing
- b. Text Continuity
- c. Holes through Data
- d. Other Missing Text
- e. Illegible Text
- f. Duplicate Text
- g. Brief Description
- h. Sequence Listing
- i. Appendix
- j. Amendments
- k. Other

## CLAIMS

- a. Claim(s) Missing
- b. Improper Dependency
- c. Duplicate Numbers
- d. Incorrect Numbering
- e. Index Disagrees
- f. Punctuation
- g. Amendments
- h. Bracketing
- i. Missing Text
- j. Duplicate Text
- k. Other

## MESSAGE

Please provide the following information

a) Index of claims

b) Issuing classification

initials

## RESPONSE

initials

# INFORMATION DISCLOSURE CITATION

(Use several sheets if necessary)

Docket Number (Optional)

1351827.0187

Application Number

10/227,380

Applicant(s)

ROSS et al.

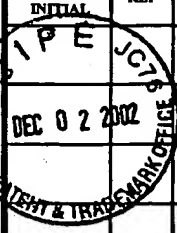
Filing Date

August 26, 2002

Group/Art Unit

2818

## U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
		3,701,980	10/31/1972	Mundy			
		4,831,585	05/16/1989	Wade et al.			
		4,799,192	01/17/1989	Wade et al.			
		6,320,777	11/20/2001	Lines et al.			
		5,694,355	12/02/1997	Skjaveland et al.			
		6,061,277	05/09/2000	Lines			
		5,973,985	10/26/1999	Ferrant			
		5,383,146	01/17/1995	Threewitt			
		6,005,790	12/21/1999	Chan et al.			
		4,791,606	12/13/1988	Threewitt et al.			

## FOREIGN PATENT DOCUMENTS

REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						YES	NO
	11260063	09/24/1999	JP				
	08235867	09/13/1996	JP				
	05062474	03/12/1993	JP				

## OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

	Koichi Takeda, et al., <u>A 16Mb 400MHz Loadless CMOS Four-Transistor SRAM Macro</u> , ISSCC, 2000.
	Kenneth Schultz, <u>Content-Addressable Memory Core Cells - A Survey</u> , Integration, The VLSI Journal, 23 (1997) pages 171 to 188

EXAMINER

DATE CONSIDERED

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Form PTO-A820  
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P09A/REV04

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SHEET 1 OF 2

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						YES	NO

## OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

		Noda K et al: "1.9-MUM2 Loadless CMOS Four-Transistor SRAM Cell in a 0.18-MUM Logic Technology", San Francisco, CA, Dec. 6 -9, 1998, New York, NY: IEEE, US, December 6, 1998 pages 643 - 646, XP000859455, ISBN: 0-7803-4775-7

EXAMINER

DATE CONSIDERED

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